

Notice of References Cited

Application/Control No.

09/840,306

Applicant(s)/Patent Under
Reexamination
MIZUNO ET AL.

Examiner

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Art Unit

2829

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,789,292	07-1998	Yamazaki et al.	438/257
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Silicon Processing for the VLSI Era by Wolf, Vol. 2, Chapter 2, Lattice Press, California, 1990.
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	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.